



Introducing the Cupria™ Development System rm-CDS 6000

optimized for broadband transport over copper . . .



Engineered to simplify the testing, evaluation and qualification process for R&D and product development teams:



rm-CDS 6000
Core Solution

Target Markets

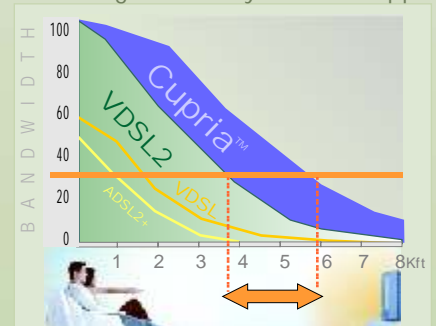
- DSLAM & CPE for residential broadband services
- Integrated HDTV, gaming, & internet receivers / set top boxes
- Metro-Ethernet transport for enterprise applications
- Secure government communication networks
- Industrial process monitoring integrating HDTV & VoIP
- Municipal surveillance, Intelligent Traffic Management and wireless mesh networks
- Backhaul applications supporting cell towers, DSLAMs, WiFi, & WiMAX
- Hi-rise MDU / MTU access
- Campus Networks

- End-to end system emulation platform supporting the Cupria™ 5101a line card
- Fully customizable test bench including industry-standard backhaul options supporting GigE rates
- GigE backplane provides multiple I/O channels
- Flexible 9-port architecture supports both high-density 1-ru access shelves and mixed mode implementation for set top box configurations
- Field-hardened cabinet for NEBS-compliant in-field deployment and stress testing
- Optional network processor modules let equipment makers configure the rm-CDS6000 platform to be compatible with their existing product development programs

Cupria™ is the only xDSL chipset optimized to meet the rigorous demand of high-fidelity IP over copper

- Enhanced QoS - Cupria™ supports 4 discrete SLA's compared to only two for VDSL2 - significantly reducing packet loss for IPTV networks.
- Four priority levels supports discrete video, VoIP, data, and IGMP queuing streams to deliver superior voice and video quality.
- Inter-frame preemptive bit queuing allows a mix of contending SLA's within single DSL frames to increase transport efficiency.
- Cupria's™ R-QAM multitone modulation efficiently uses every hertz of the copper spectrum, yet requires far fewer tones to achieve faster synching and higher payload ratios with lower bit-error rates than VDSL2.
- Longer copper reach serves 30 to 40% larger distribution area than VDSL2 - cutting IPTV access network costs significantly.

Delivering Hi-Fidelity IP over Copper



30-40% GREATER RATE & REACH

For further information, please go to www.rimsemi.co member of IPSLSig





rm-CDS 6000 Cupria™ Development System

System Architecture Engineered for Rapid Prototyping and Field Evaluation within a 1-ru Shelf.

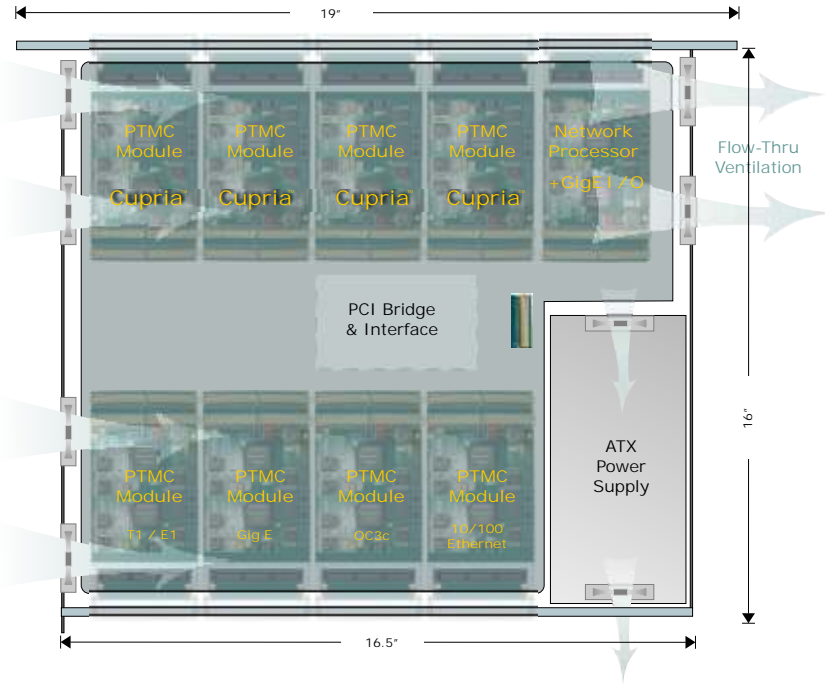
Flexible PTMC modules let you tailor the Cupria™ Evaluation System to match your prototyping and/or evaluation needs

Field-hardened chassis delivers 130 CFM cooling for NEBS-compliant deployment in remote terminals

Multi-GigE backplane can support data rates adequate for 8 IPTV lines or 8 lines of 200Mbps symmetric enterprise data

Each of the rm-CDS 6000 ports can be configured using off-the-shelf universal PTMC modules to implement a mix of transport and service applications including:

- Cupria™ IPSL™ interface
- GigE
- GPON
- HDSL2
- T1 / E1
- MPEG2 / 4 video decoder
- WiFi networks
- VoIP
- Bluetooth / USB



Cupria™ Access Transport Module - Future-Proofing your Investment

The Cupria™ 5101a will initially ship with two FPGAs per line card. Future modules will use one ASSP to enable higher densities and draw less power.



Options for Industry-Standard Network Interfaces

Commercially available PTMC backhaul modules let you tailor the Cupria™ Evaluation Platform using industry-standard network interface options to perfectly fit your specific networking preferences.

- OC3
- GPON
- T1/E1
- HDSL2
- GigE

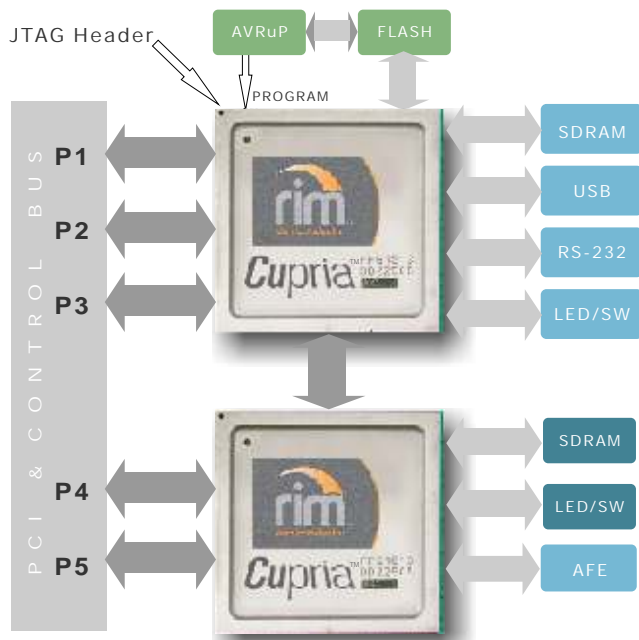
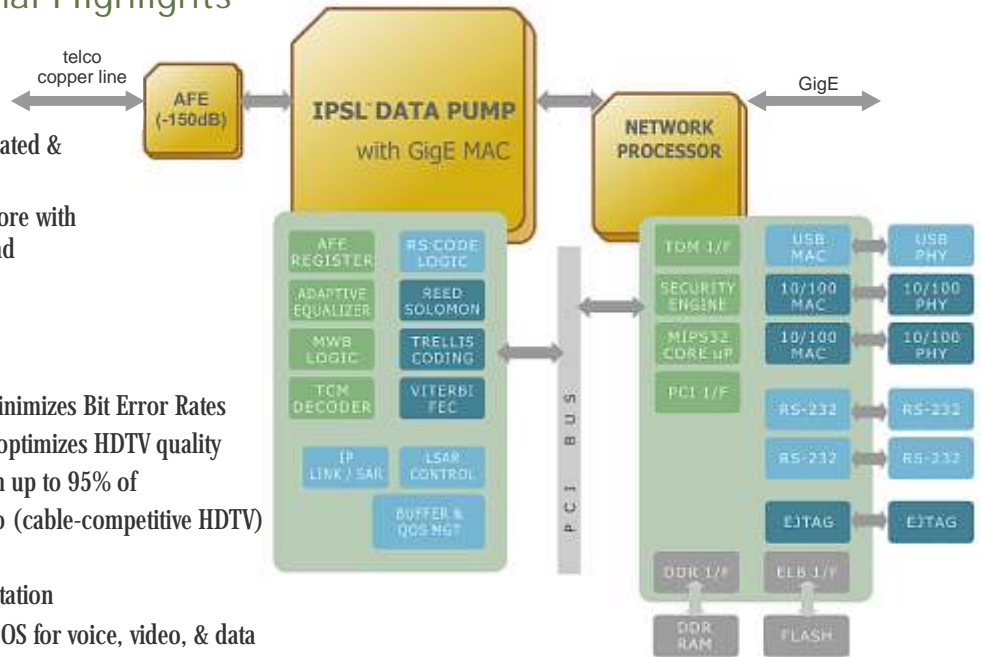
Network Processor Options Speed Development by providing compatibility with your existing product line

You can select your choice of optional network processors to speed development. Selecting a processor compatible with your existing object code means the rm-CDS 6000 will interoperate with existing products requiring minimal development.

- x86
- MIPS
- ARM
- PowerPC

Cupria™ System Functional Highlights

- Service Aware QOS Engine
- Link/SAR Hardware Engine
- DMA/Multiport Memory Engine for automated & chained transactions
- High Performance, Multi-Threaded CPU core with DSP functionality for Enhanced inter-thread communications support
- TR-69 compliant
- Triple-Play over pure IP transport
- Advanced R-QAM Encoding / Decoding minimizes Bit Error Rates
- Enhanced QOS with grooming at the CPE optimizes HDTV quality
- Capable of Symmetric or Asymmetric, with up to 95% of bandwidth available for downstream video (cable-competitive HDTV)
- Improved Signal-to-Noise Tolerance
- -150 dBm/Hz Front-end Noise Floor Adaptation
- Dynamic Rate Re-partitioning preserves QOS for voice, video, & data



Cupria™ 5101a Line Card Block Diagram

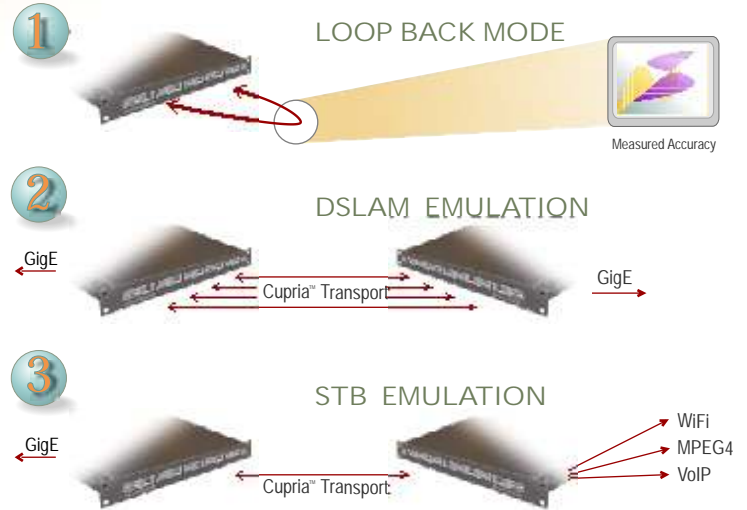
- Mechanically and electrically compliant to standard PTMC specifications
- Interfaces to PCI 32/33 or PCI 64/66
- Offers multiple interfaces, fast on-board memory resources and two Cupria FPGAs
- Interfaces to an AFE board

9-port Platform Supports Multiple Evaluation Configurations

This single-chassis option supports testing of enhanced performance using a “Loop Back” mode.

Supporting a more robust field test, this pair of chassis can operate in real-world, field-hardened sites serving 8 lines of Cupria™ transport, using the GigE backhaul on the ninth card, or your choice of optional backhaul transports.

For the CPE manufacturer, this dual-chassis option lets you serve the Cupria™ transport from one station while the second unit is configured for set top box emulation using industry-standard PTMC service application modules.



rm-CDS 6000 Specifications

- Complete end-to-end test system supports Cupria™ line card
- 1-ru rack mount cabinet
- Universal PTMC motherboard with 8 user-configurable PTMC sites
- Multi-GigE backplane supports multiple I / O paths
- Industry-standard network processor options on ninth PTMC card
- ATX power supply

Hardware and software features discussed here are subject to change without notice. Actual product features could differ materially from expectation for reasons including but not limited to following: product development difficulties, market demand and acceptance of products, the impact of changing economic conditions, business conditions in the Internet and telecommunications industries, reliance on third parties, including potential suppliers, licensors, and licensees. Rim Semiconductor is under no obligation to revise or update any statement in order to reflect events or circumstances that may arise in the future. rm-CDS 6000 pb rel061507 1